Nageru: Taking free software video mixing into 2016

FOSDEM, January 30th 2016

Steinar H. Gunderson

投げる 【なげる】 (v1,vt) (1) to throw; to cast away

投げる 【なげる】 (v1,vt) (1) to throw; to cast away

(2) to face defeat; to give up;

- Primary goals:
 - High-quality
 - High-performance
 - Usable audio tools

- Nice to have:
 - Suitable for Debian main
 - HTML5 overlay graphics (and/or integration with CasparCG)
 - A pony



Ilja van Sprundel Director of Penetration Testing







Quote - Sanders' fired staffer: Josh Uretsky

He was trying to "understand how badly the Sanders campaign's data was exposed" and not attempting to take data from the Clinton campaign".

"We knew there was a security breach in the data, and we were just trying to understand it and what was happening," said Josh Uretsky

He added, "To the best of my knowledge, nobody took anything that would have given the (Sanders) campaign any benefit."

"This wasn't the first time we identified a bad breach," he said, confirming to CNN that the Sanders campaign reported another breach to the DNC in October. "We reported it to them. They thanked us for reporting it and they told us the breach had been closed."

"In retrospect, I got a little panicky because our data was totally exposed, too," Uretsky said of how he handled the latest breach. "We had to have an assessment, and understand of how broad the exposure was and I had to document it so that I could try to calm down and think about what actually happened so that I could figure out how to protect our stuff."

From YouTube:

#Bernie 2016

RM S: How dare they to question Sanders ethics!



Veselin Topalov

Alder: 40 Verdensranking: FIDE-rating: 2816







19. TREKK

86% 7% 00:08:40

01:28:34

MAGNUS CARLSEN







```
MMX (1997)

corns or 6 = hine(f * 206.0), for * 226 + 6, ROUND * 126;

corns or 6 = mm, set row, set 60, cond-we = _em, set 1_p16(ROUND);

corns _em6 + bee _ _em, set _em1 frolls, fervore = _em1, set _em1 froll froll;

corns _em6 + bee _ em_, set _em1 frolls, fervore = _em2, set _em1 froll froll;

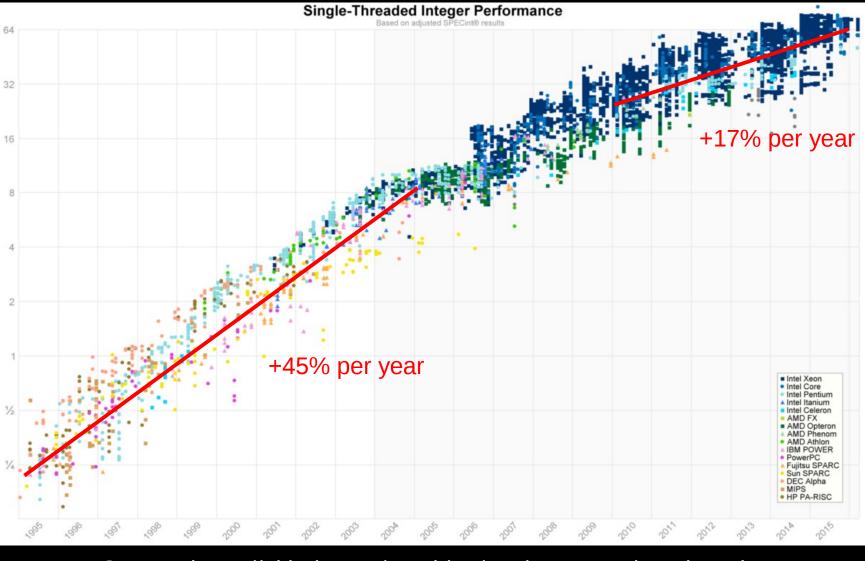
corns _em6 + bee _ em_, set _em1 frolls, fervore = _em2, set _em2 froll froll;

corns _em6 + bee _em2, set _em2 froll froll, froll fr
```

Input 1 (720p59,94)

Input 2 (720p59,94)





Source: https://github.com/preshing/analyze-spec-benchmarks

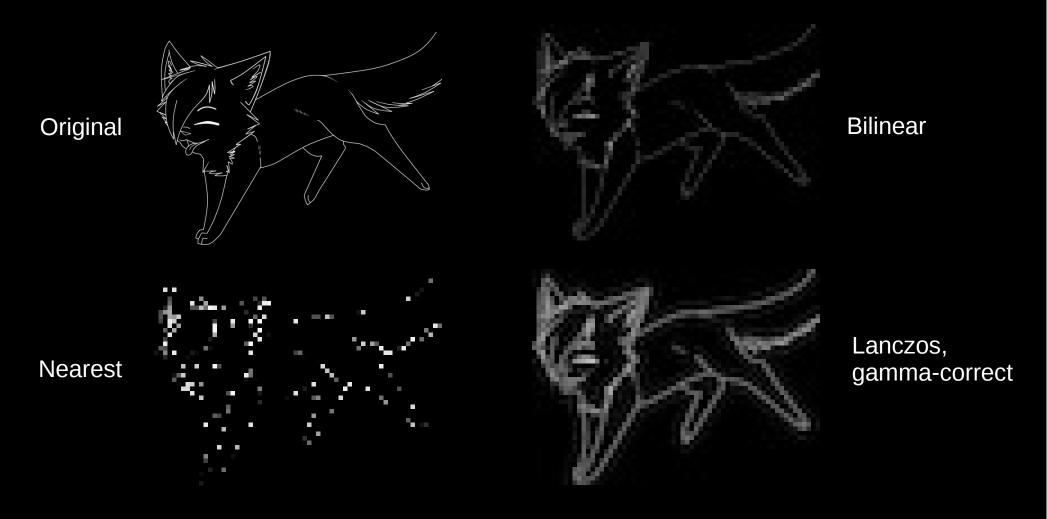
Cc: Markus Rechberger

From: Steinar H. Gunderson

Subject: [PATCH] Add support for usbfs zerocopy.

From: Steinar H. Gunderson

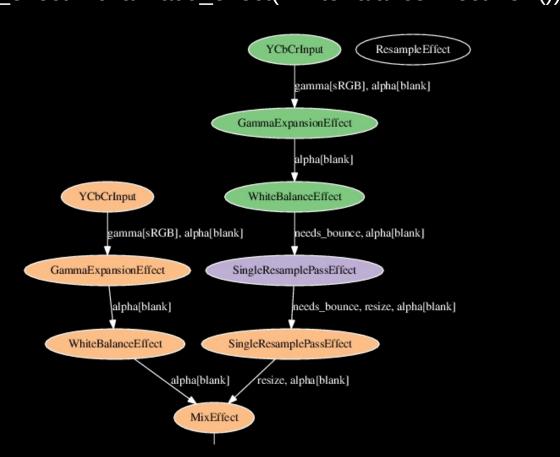
Subject: [PATCH] [libusb] Add support for persistent device memory



Drawing by sillycandy, CC-BY 3.0

```
function make_fade_input(chain, signal, live, deint, scale)
    local input, wb_effect, resample_effect, last
    if live then
        input = chain:add_live_input(false, deint)
        wb_effect = chain:add_effect(WhiteBalanceEffect.new())
```

theme.lua



Movit filter graph

```
const int fi = Irintf(f * 256.0), finv = 256 - fi, ROUND = 128;
int i = 0;
int i =
```

Preview Live

Fade

Cut



```
MMX (1997)

constant 6 = biret(1 * 206.10), for = 206 * 8, ROUND = 108;

constant 6 = biret(1 * 206.10), for = 206 * 8, ROUND = 108;

constant 6 = biret(1 * 206.10), for = 206 * 8, ROUND = 108;

constant 7 = biret(1 * 206.10), for = 206.10, ROUND = 108;

constant 7 = biret(1 * 206.10), for = 206.10, ROUND = 206.10, R
```





Input 1 (720p59,94) Set WB Input 2 (720p59,94)

Set WB

```
MMX (1997)
const int fi = Irintf(f * 256.0), finv = 256 - fi, ROUND = 128;
const m64 zero = mm_setzero_si64(), roundvec = mm_set1_pi16(ROUND);
const __m64 fvec = _mm_set1_pi16(fi), finvvec = _mm_set1_pi16(finv);
const __m64 *avec = (__m64 *)a, *bvec = (__m64 *)b;
  m64 *outvec = ( m64 *)out;
for ( ; i < width * height; i += 8) {
       __m64 aval = avec[i / 8], bval = bvec[i / 8];
       __m64 a_lo = _mm_unpacklo_pi8(aval, zero), a_hi = _mm_unpackhi_pi8(aval, zero);
       __m64 b_lo = _mm_unpacklo_pi8(bval, zero), b_hi = _mm_unpackhi_pi8(bval, zero);
__m64 out_lo = _mm_add_pi16(_mm_mullo_pi16(finvvec, a_lo), _mm_mullo_pi16(fvec, b_lo));
        __m64 out_hi = _mm_add_pi16(_mm_mullo_pi16(finvvec, a_hi), _mm_mullo_pi16(fvec, b_hi));
      out lo = _mm_srli_pi16(_mm_add_pi16(out_lo, roundvec), 8);
out_hi = _mm_srli_pi16(_mm_add_pi16(out_hi, roundvec), 8);
outvec[i / 8] = _m_packuswb(out_lo, out_hi);
for (; i < width * height; ++i) {
       out[i] = (a[i] * fi + b[i] * finv + ROUND) >> 8;
```

Cut



Fade



Preview

Live

-8,5 RST

```
MMX (1997)
rest int fi = kintfiff * 256.0), finv = 256 - 6, ROUND = 128;
  ; i < width * height; ++i) {
out(i) = (a)i] * fi + b(i) * finv + ROUND) >> 8;
```

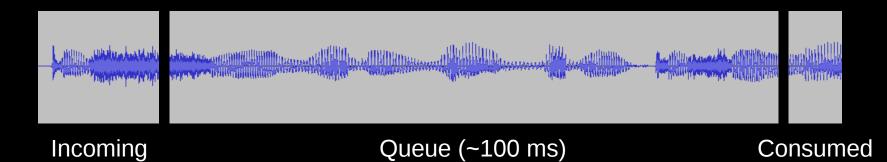




Input 1 (720p59,94) Set WB

Input 2 (720p59,94)

Set WB



Const int fi = lrintf(f * 256.0), finv = 256 - fi, ROUND = 128; int i = 0; in

Live -8,5 RST

Lo-cut (24dB/oct)



✓ Enabled







Input 1 (720p59,94) Set WB Input 2 (720p59,94)

Cut

Fade

Set WB

Live -8.5 RST

Preview

Lo-cut (24dB/oct) Gain staging

-0

121 Hz

+13,0 dB

▼ Enabled ▼ Auto



MMX (1997)

cores or 6 = hint(f) * 206.(j), 6nv = 206 - 6. ROUND = 120;

or 1 = 0;

cores = m6.5 zero = mn_setzero_s660, roundesc = mn_setf_p1660UND;

cores = m6.5 zero = mn_setzero_s660, roundesc = mn_setf_p1660UND;

cores = m6.5 zero = mn_setzero_s660, roundesc = mn_setf_p166nv;

core = m6.5 zero = mn_setzero_s660, roundesc = mn_setf_p166nv;

core = m6.5 zero = mn_setzero_s660 zero = mn_setzero_s660v;

core = mn_setzero_s660v;

cor





Input 1 (720p59,94)

Set WB

Input 2 (720p59,94)

Set WB

Cut

Fade

```
MMX (1997)
const int fi = Irintf(f * 256.0), finv = 256 - fi, ROUND = 128;
const m64 zero = mm_setzero_si64(), roundvec = mm_set1_pi16(ROUND);
const __m64 fvec = _mm_set1_pi16(fi), finvvec = _mm_set1_pi16(finv);
const __m64 *avec = (__m64 *)a, *bvec = (__m64 *)b;
  m64 *outvec = ( m64 *)out;
for (; i < width * height; i += 8) {
       __m64 aval = avec[i / 8], bval = bvec[i / 8];
       __m64 a_lo = _mm_unpacklo_pi8(aval, zero), a_hi = _mm_unpackhi_pi8(aval, zero);
      __m64 b_lo = _mm_unpacklo_pi8(bval, zero), b_hi = _mm_unpackhi_pi8(bval, zero);
__m64 out_lo = _mm_add_pi16(_mm_mullo_pi16(finvvec, a_lo), _mm_mullo_pi16(fvec, b_lo));
        _m64 out_hi = _mm_add_pi16(_mm_mullo_pi16(finvvec, a_hi), _mm_mullo_pi16(fvec, b_hi));
      out lo = _mm_srli_pi16(_mm_add_pi16(out_lo, roundvec), 8);
out_hi = _mm_srli_pi16(_mm_add_pi16(out_hi, roundvec), 8);
outvec[i / 8] = _m_packuswb(out_lo, out_hi);
for (; i < width * height; ++i) {
       out[i] = (a[i] * fi + b[i] * finv + ROUND) >> 8;
                                                          Preview
```



Lo-cut (24dB/oct) Gain staging Compr. threshold

-26.0 dB

121 Hz +13.0 dB ▼ Enabled ✓ Enabled ✓ Auto



MMX (1997) rest int fi = kintfif * 256 (), finv = 256 - fi, ROUND = 126; i < width * height; ++i) {
out[i] = (a[i] * fi + b[i] * finy + ROUND) >> 8.





Input 1 (720p59,94) Set WB

Input 2 (720p59,94)

Set WB

Cut

Fade

```
Const int fi = Irintf(f * 256.0), finv = 256 - fi, ROUND = 128;
int i = 0;
in
```

Live -8,5 RST

Lo-cut (24dB/oct) Gain staging Compr. threshold Limiter threshold



▼ Enabled



✓ Auto





✓ Enabled

✓ Enabled



MMX (1997)

contain 6 = bind(* *255 (i), five = 250 - 6, ROUND = 120;

contain 6 = bind(* *255 (i), five = 250 - 6, ROUND = 120;

contain 6 = bind(* *255 (i), five = 250 - 6, ROUND = 120;

contain = 64 zero = _mm_setzero_u66().condwee = _mm_set1_p16(ROUND);

contain = 64 zero = _mm_setzero_u66().condwee = _mm_set1_p16(RoUND);

contain = 64 zero = _mm_setzero_u67(zero = 100 zero = 10





Input 1 (720p59,94)

Set WB

Input 2 (720p59,94)

Set WB

Cut

Fade

```
MMX (1997)
const int fi = Irintf(f * 256.0), finv = 256 - fi, ROUND = 128;
const m64 zero = mm_setzero_si64(), roundvec = mm_set1_pi16(ROUND);
const __m64 fvec = _mm_set1_pi16(fi), finvvec = _mm_set1_pi16(finv);
const __m64 *avec = (__m64 *)a, *bvec = (__m64 *)b;
  m64 *outvec = ( m64 *)out;
for (; i < width * height; i += 8) {
      __m64 aval = avec[i / 8], bval = bvec[i / 8];
      __m64 a_lo = _mm_unpacklo_pi8(aval, zero), a_hi = _mm_unpackhi_pi8(aval, zero);
      __m64 b_lo = _mm_unpacklo_pi8(bval, zero), b_hi = _mm_unpackhi_pi8(bval, zero);
__m64 out_lo = _mm_add_pi16(_mm_mullo_pi16(finvvec, a_lo), _mm_mullo_pi16(fvec, b_lo));
        m64 out hi = mm add pi16( mm mullo pi16(finvvec, a hi), mm mullo pi16(fvec, b hi));
      out_lo = _mm_srli_pi16(_mm_add_pi16(out_lo, roundvec), 8);
     out hi = _mm_srll_pi16(_mm_add_pi16(out_hi, roundvec), 8);
outvec[i / 8] = _m_packuswb(out_lo, out_hi);
for (; i < width * height; ++i) {
      out[i] = (a[i] * fi + b[i] * finv + ROUND) >> 8;
                                                      Preview
```



Compr. threshold Limiter threshold Makeup gain Lo-cut (24dB/oct) Gain staging



▼ Enabled



✓ Auto









▼ Enabled ▼ Enabled **✓** Auto



MMX (1997) rest int fi = kintfif * 256 (), finv = 256 - fi, ROUND = 126; i < width * height; ++i) {
out[i] = (a[i] * fi + b[i] * finy + ROUND) >> 8.

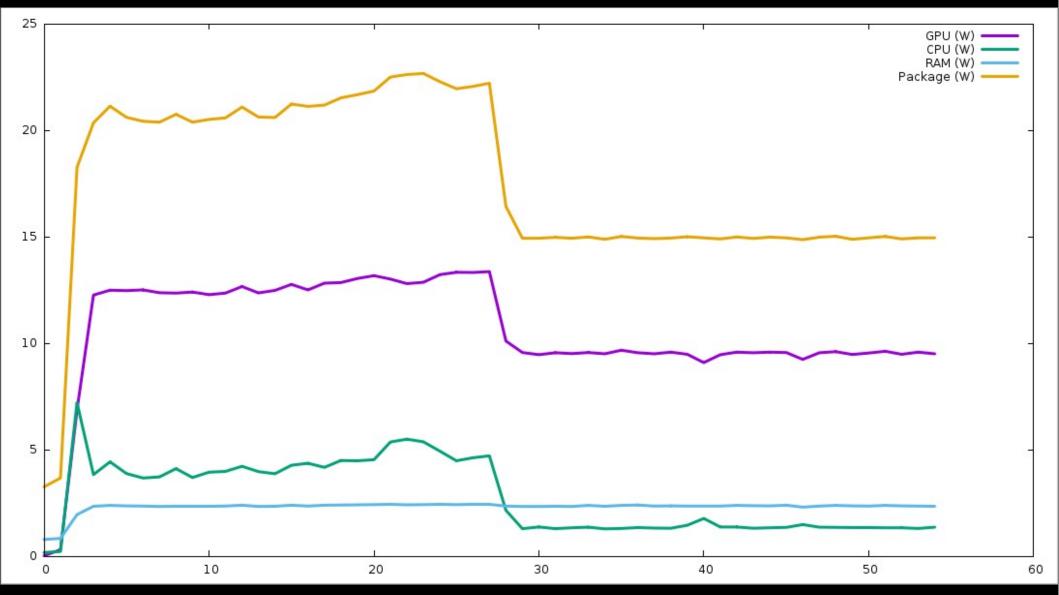




Input 1 (720p59,94) Set WB Input 2 (720p59,94) Set WB

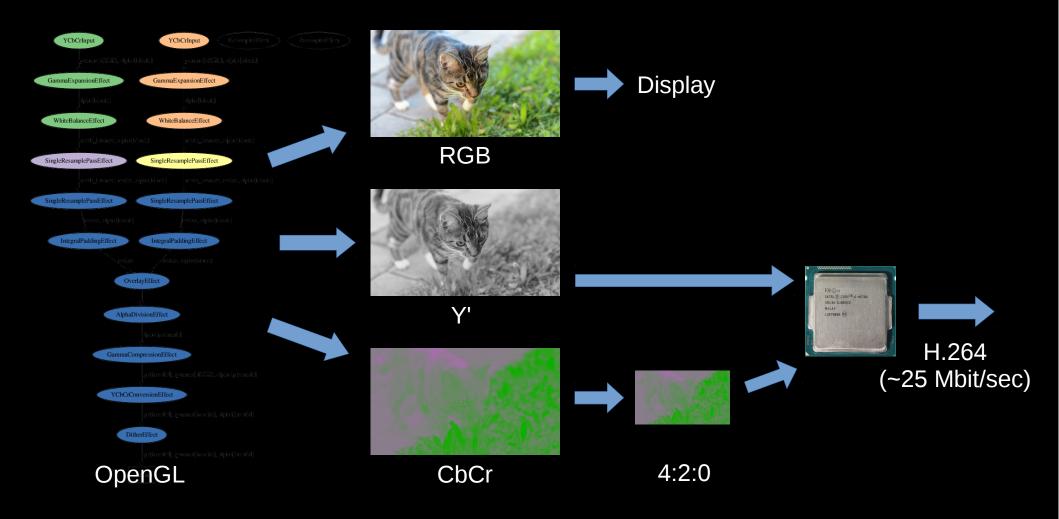
Cut

Fade



Subject: [PATCH] Add SSE2 support to zita-resampler

From: Steinar H. Gunderson



Chip picture by Kolpar1, CC-BY-SA 3.0

Live demo!

Thank you!

(Q&A if people have not run out of patience yet)

https://nageru.sesse.net/